



Tiger Lake Platform Intel® Dekel PHY Firmware

Release Notes - NDA

Revision 2.1031

February 2020

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Revision History

Revision Number	Description	Revision Date
2.1017	Initial Release	September 2019
2.1020	ES2 Support Release	October 2019
2.1021	ES2 Support Release	October 2019
2.1022	ES2 Support Release	November 2019
2.1029	ES2 Support Release	January 2020
2.1031	ES2 Support Release	February 2020

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1 Introduction

1.1 Scope of Document

This document provides component level details of the downloaded kit and the contents of each folder in the kit.

1.2 Acronyms

Term	Description
BIOS	Basic Input Output System
FW	Firmware

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2 *Issue Status Definitions*

This document provides sightings and bugs report for Intel® CSME Firmware 13.0 SKU for the Ice Lake Platform. At the time of a milestone release, this report will be distributed with the Intel® CSME Kit and will provide information on new issues and the status of old issues (replacing the Release Notes document).

Closed Issues: This category will only display closed issues within the current Intel® CSME Kit release. After each release, old issues will be dropped down to the “Archive” section and then new closed issues will take its place back up top for the next release. If an issue is posted in this section, it will indicate that the issue has been verified and fixed within the kit that is being released.

Known Issues: This category will display all Known Issues since the Alpha release and will remain in this section until fixed or noted otherwise. “Known Issues” are still under investigation and may or may not be root caused.

Archive – Fixes in Previous Kits: This category will display all closed issues that were closed in their respected kit#. This section will serve as a history of fixed issues.

Sightings listed in this document apply to the Ice Lake SKU’s unless noted otherwise in this document or in the sightings tracking systems.

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3 Intel® MG PHY New Features - RCR's

RCR Number	Change Information

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4 Closed Issues in this Release

Issue Number	Interface	Description	Fixed in Version
	TCSS	Link Stability improvements across all protocols	2.1020
	TCSS	Improve entry into Sx state at PkgC10	2.1021
1507313447	PCIE	May improve Fail Rates of Link Stability following Boot to Boot as Speed/Gen degradation, Link Width reduction, Link Drops and Hot Plug	2.1022
2209154146	PCIE	Link may be unstable during PKG-C7 and higher	
2209584308	PCIE	Fix for PCIe cold boot to mitigate false squelch detect.	2.1029
2209600175	PCIE	Tuning the TX Preset values.	
1608941417	PCIE	Fix for Link states reporting.	
1507313447	PCIE	Mitigate the false squelch detect for Gen1 and Gen2 PCIe.	
1809000793	PCIE	Rx margining tuning.	
1807972097	TCSS	Fix for clock transition during PKG C10 exit.	
1507313447	PCIE	Mitigate the false squelch detect for Gen1 and Gen2 PCIe.	2.1031

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5 *Open / Known Issues – To Date*

Issue Number	Description	Details	Affected Sku's

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6 Archive

6.1 Fixed in Previous Kits

N/A

6.2 RCR's

N/A

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